

JOB DESCRIPTION – DESIGN ENGINEERING SR. MANAGER/MANAGER

POSITION SUMMARY:

The Manager of Design Engineering will be responsible for leading a local team of Design and Layout personnel. The manager will have a variety of responsibilities including

- Guidance, Direction and Decision making where authority is necessary for the team
- Perform as an Active Technical member including Design and Leadership at times
- Performance reviews & Goal setting aligned with VP
- Forward looking initiatives to drive improved execution
- Strong interaction and collaboration with other Design Managers
- Strong interaction with other functional teams such as Applications, Test & Product Engineering, Packaging, Technology and Quality
- Communication to the local team and Communication to the VP of Engineering

REPORTS TO: VP of Engineering

LOCATION: Raleigh

QUALIFICATIONS:

Education: BSEE Required; MSEE or PhD preferred

Experience: 3+ years leading Design teams
10+ years professional experience in analog and mixed signal design
Experience with all manner of analog cells
Experience with Power Management design
Experience serving as design lead on projects
Proven track record taking products from initial definition to mass production
Experience working with cross-functional teams

Core Competencies: Strong Leadership skills
Recognize and predict issues
Strong knowledge of analog integrated circuit fundamentals
Strong knowledge of CMOS device physics and fabrication processes
Strong knowledge of parasitic and noise analysis
Strong knowledge of best Layout practices
Continuous improvement mindset
Possess strong written and verbal communication skills

ESSENTIAL DUTIES & RESPONSIBILITIES:

1. Day-to-day leadership of local Design and Layout teams
2. Work with VP on pre-Business Review Activities: Very early product definition support & resource planning
3. Research and development of state-of-the-art analog and mixed signal products for selected markets
4. Collaborate with other Design managers and VP to develop best practices for all of Design and Layout
5. Develop products to meet performance and cost targets per the schedule created by Design and Layout
6. Provide guidelines to Layout designers on circuit layout
7. Travel 1 or 2 times per year
8. Work with other cross-functional teams and ensure information flows in both directions effectively