Director of Engineering, Analog

Type: Full-time
Reports To: VP of Engineering, ACDC
Location: San Diego, California or Toronto, Canada

Company Description

Silanna is a privately held semiconductor company with exciting new products for power conversion. Silanna's power products have industry leading metrics of power density, efficiency and switching frequency. Our products are designed in a true mixed signal flow, having approximately 50% area split between analog and digital sections.

Job Description and Responsibilities

This position has both technical and managerial responsibilities. This position will manage the Analog Engineering Staff of the ACDC business unit and will be responsible for the staffing, recruiting, training, directing, and performance of this group. This position will be responsible for managing and staffing multiple concurrent projects and should be technically involved in scheduling, reviewing, and progress monitoring of the projects.

Essential Duties and Responsibilities

As a Director of Engineering, your work will be critical to the success of the AC-DC power business. As such, your responsibilities will include the following:

- Participate in all phases of IC development including architecture, transistor-level analog circuit reviews, design-layout interaction, simulation plans and methodology, scheduling and tracking.
- Provide guidance & design expertise during design reviews, take a hands on approach during design reviews relying on past analog design experience
- Work with cross functional teams (process, test, product, QA, etc.) to create the highest performance development process
- Recruit and train the analog design team
- Work with cross site design teams setting priorities, direction, and tracking progress.

Required Skills and Experience

- 15+ years of IC design experience, BSEE Required, MSEE preferred
- In-depth technical knowledge of analog IC design
- Experience and CMOS device physics and fabrication processes
- Experience with Ultra high voltage processes would be beneficial
- Experience with power management, AC-DC or DC-DC converters is useful
- Some knowledge of digital design, understanding of verification methodologies, and knowledge of Verilog are preferred
- Self-motivated, driven, results oriented that meets project deliverables
- Willing to travel (25%)