

Director of Design Engineering

Type: Full-time
Reports To: Vice President, Power Engineering
Location: San Diego, CA

The Director of Design Engineering will be responsible for leading a local team of Design and Layout personnel. The manager will have a variety of responsibilities including:

- Day-to-day leadership of Design and Layout teams
- Work with VP on pre-Business Review Activities: Very early product definition support & resource planning
- Research and development of state-of-the-art analog and mixed signal products for AC/DC markets
- Collaborate with other Design managers and VP to develop and enhance best practices for all of Design and Layout
- Develop products to meet performance and cost targets per schedule
- Travel – up to 10% of your time
- Work with other cross-functional teams to ensure information flows in both directions effectively. Guidance, Direction and Decision making where authority is necessary for the team
- Perform as an Active Technical member of the team
- Performance reviews & Goal setting aligned with VP
- Forward looking initiatives to drive improved execution
- Strong interaction with other Design Directors and Managers
- Strong interaction with other functional teams including Applications, Technology, Test & Product Engineering, Packaging, and Quality
- Communication to the local team and Communication to the VP of Engineering

QUALIFICATIONS AND EDUCATION REQUIREMENTS

- BSEE Required; MSEE or PhD preferred
- 10+ years leading Design teams
- 12+ years professional experience in Power Management including analog and mixed signal design
- Experience serving as a design lead
- Experience with all manner of analog cells
- Experience with Power Management design
- Proven track record taking products from initial definition to mass production
- Experience working with cross-functional teams
- Strong Leadership skills
- Creative and Innovative
- Strong knowledge of analog integrated circuit fundamentals
- Strong knowledge of CMOS device physics and fabrication processes
- Strong knowledge of parasitic and noise analysis
- Knowledge of 1.8V to 700V processes
- Strong knowledge of best Layout practices
- Recognize and predict issues and react to them effectively
- Continuous improvement mindset
- Possess strong written and verbal communication skills
- Self-motivated, driven, and passionate individual with a focus on results
- Ability to influence outside the reporting change to help meet Design & Development requirements