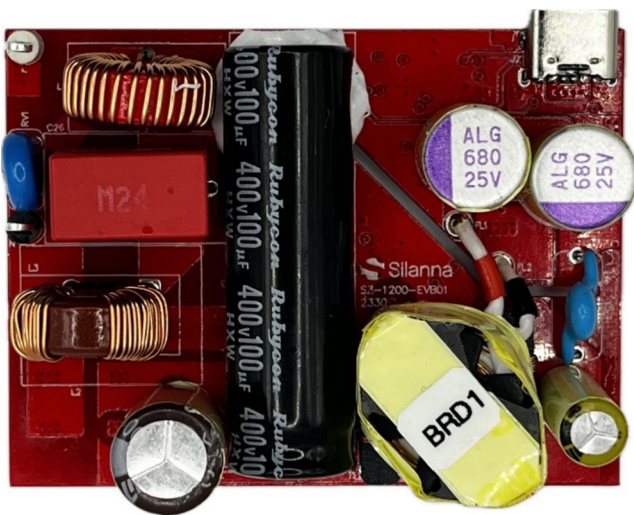


65W 1C USB-PD Evaluation Board using Silanna Semiconductor's High Frequency ACF Controller with Integrated GaN Primary FET and X-Cap Discharge Circuit (EVB-1)

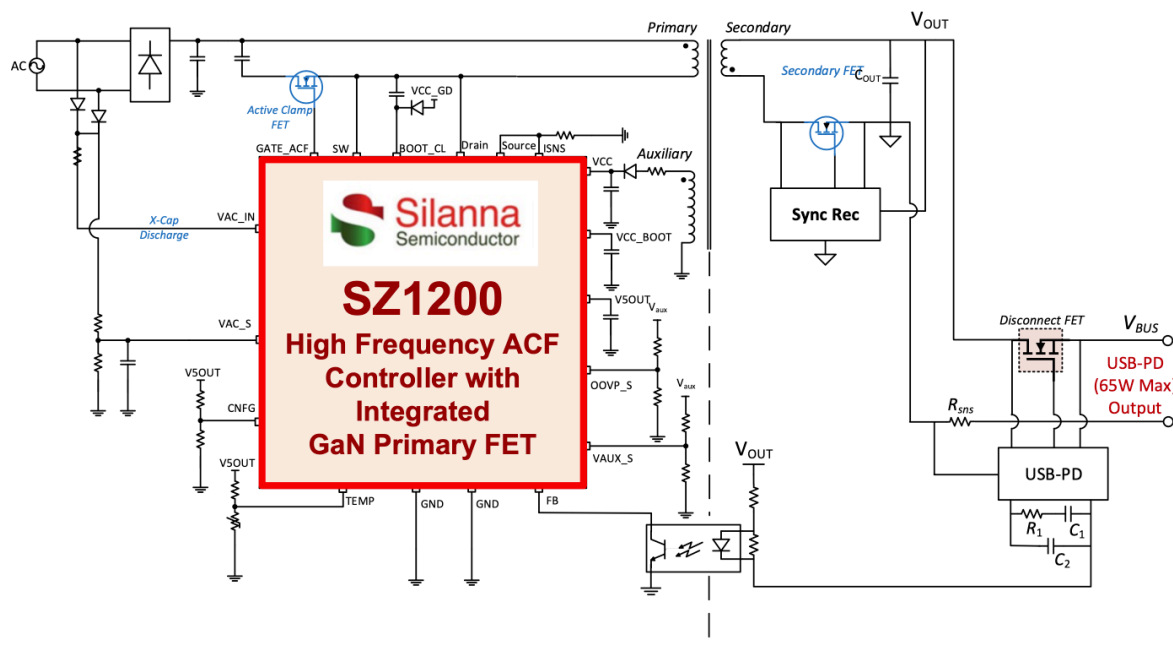
Silanna Semiconductor's SZ1200 delivers best-in-class power density and performance



Key Specs:

- Input: 90-265VAC
- Output Power: 65W
- Output Ports: 1C
- Type-C Single Port: 65W Max (5V/3A, 9V/3A, 15V/3A, 20V/3.25A)
- Transformer: RM-7 for High Power Density

Schematic

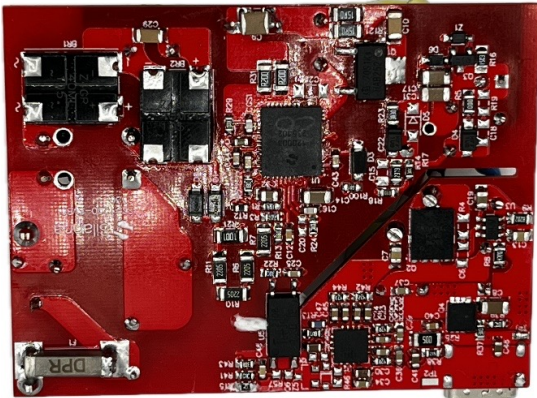


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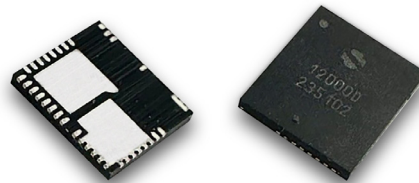
EVB-1 Features:

- 65W 1C evaluation board using SZ1200 High Frequency ACF controller
- > 92% end-to-end peak efficiency
- < 25 mW no-load power consumption
- > 6 dB conducted EMI margin
- Very high density with RM-7 transformer
- Up to 225% peak output power supporting notebook applications
- Schedule
- Engineering Sample Release in Apr'24
- Production Release at the end of Q2'24



SZ1200 Features:

- Configurable high-switching-frequency operation (up to 250 kHz)
- Integrated 700 V GaN primary FET
- Integrated UHV X-cap discharge circuit, active clamp driver and start-up regulator
- Over 93% efficiency at low line
- Flat efficiency across universal input voltage (90 - 265 Vac) and loading conditions
- CCM for increased peak power delivery and better utilization of transformer core in universal input applications
- QR valley mode switching and boosting for low EMI and near-ZVS operation
- OptiMode™ cycle-by-cycle adaptive digital control
- Self-tuning valley detection
- OTP, OVP, OCP, OOPP and OSCP protection
- Space-saving 8 mm X 7 mm QFN Package



Preliminary Efficiency :

